

7-27-00

THE UNITED STATES PATENT AND TRADEMARK OFFICE

DAC



Applicant: Tremblay, Marc  
Assignee: Sun Microsystems, Inc.  
Title: A Grouping Logic Circuit In A Pipelined Superscalar Processor  
Serial No.: 08/662,582 Filing Date: 06/11/96  
Examiner: Clark, S. Group Art Unit: 2815  
Docket No.: SP-1391 US

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San Jose, California  
July 25, 2000

BOX DAC  
COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

OFFICE OF PETITIONS

Petition under 37 C.F.R. §1.53(e)

Dear Sir:

In response to the Notice of Improper CPA Filing under 37 C.F.R. § 1.53(d), Applicants hereby petition the Commissioner to treat Applicants' Continued Prosecution Application (CPA) Request of August 2, 1999 as an application under 37 C.F.R. § 1.53(b). In accordance with 37 C.F.R. § 1.53(e), Applicants hereby authorize the Commissioner to deduct from Deposit Account 19-2386 the amount of \$130, being the amount specified under 37 C.F.R. § 1.17(i), and any additional necessary amount for this petition. Applicants also enclose a copy of the Specification, drawings and signed declaration submitted in the prior application, as required under 37 C.F.R. § 1.53(b), and a Preliminary Amendment.

If the Examiner has any questions regarding the above, the Examiner is respectfully requested to telephone the undersigned Attorney for Applicants at 408-453-9200.

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail, Express Mail Label No. EL 487 695 809 US addressed to: Commissioner for Patents, Box DAC, Washington, D.C. 20231, on July 25, 2000.

Attorney for Applicant

7/25/2000

Date of Signature

Respectfully submitted,

Edward C. Kwok  
Attorney for Applicant  
Reg. No. 33,938

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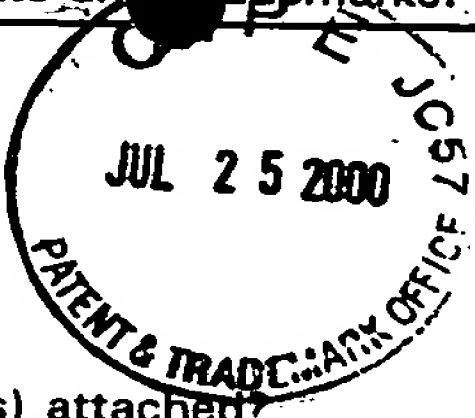
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# PATENTS ONLY

To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Marc Tremblay



Additional name(s) of conveying party(ies) attached?

☐ Yes ☒ No

3. Nature of Conveyance:

- ☒ Assignment ☐ Merger  
☐ Security Agreement ☐ Change of Name  
☐ Other \_\_\_\_\_

Execution Date: June 6, 1996

2. Name and address of receiving party(ies):

Name: Sun Microsystems, Inc.

Internal Address: \_\_\_\_\_

Street Address: 2550 Garcia Avenue

City Mountain View State CA ZIP 94043

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

this document is being filed together with a new application, the execution date of the application is: June 6, 1996

A. Patent Application No.(s)

entitled "PIPELINED INSTRUCTION DISPATCH UNIT IN A SUPERSCALAR PROCESSOR"

B. Patent No.(s)

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Edward C. Kwok

Internal Address: SKJERVEN, MORRILL, MacPHERSON,

FRANKLIN & FRIEL

Street Address: 25 METRO DRIVE, SUITE 700

City SAN JOSE State CA ZIP 95110

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41): .....\$ 40.00

- ☒ Authorized to be charged to Deposit Account 19-2386  
☒ Charge Deposit Account 19-2386 for any additional fees required for this conveyance and credit deposit account 19-2386 any amounts overpaid

(Duplicate copy of this page attached)

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Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Edward C. Kwok 33,938

Name of Person Signing

*Edward C. Kwok*  
Signature

June 11, 1996

Date

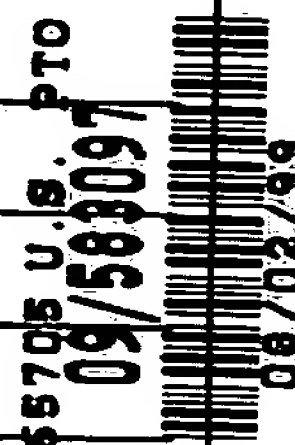
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Total number of pages comprising cover sheet: 2

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June 11, 1996  
of Signature

*Robert Parker*



ASSIGNMENT

For good and valuable consideration, receipt of which is hereby acknowledged, I, Marc Tremblay of Palo Alto, California, hereby sell, assign and transfer to Sun Microsystems, Inc., a Delaware corporation, having a place of business at 2550 Garcia Avenue, Mountain View, California 94043, its successors and assigns, the entire right, title and interest throughout the world in my invention in

PIPELINED INSTRUCTION DISPATCH UNIT IN A SUPERSCALAR PROCESSOR

for which I have executed a United States patent application on or about the date of this assignment, and all patent applications and patents of every country for said invention, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; I authorize the above-named assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from me; I agree to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said assignee, its successors, assigns and nominees, at their request and expense, in obtaining and enforcing patents for said invention in all countries; and I request the Commissioner of Patents and Trademarks to issue all patents granted for said invention to the above-named assignee, its successors and assigns.

Executed this 6 day of June, 1996.

  
Marc Tremblay